OCT 0 2 2003

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

tt) Welsking

In re l	U.S. Patent Application of	)			. 1	
SATOH et al.  Application Number: 09/914,429		) )	) )			
		)	Examiner Thompson, Annette M			
Filed:	November 5, 2001	)	Tuompso	ii, Aiii	ictic i	•
For:	SEMICONDUCTOR INTEGRATED CIRCUIT HAVING SELF-TESTING CIRCUIT AND METHOD FOR DESIGNING THE SAME (AMENDED)	) ) )		LECHNOLOGY	00	굒
Attorney Docket No. HITA.0099		)		30 A516	OCT 10	RECEIVE
for	rable Assistant Commissioner Patents ington, D.C. 20231		;	CENTER 2800	2003	
	PETITION FOR EXTENSION OF	F TIME	:	0		

Sir:

Applicants request that the time for taking action in this case be extended pursuant to 37 C.F.R. § 1.136(a) for two (2) months. The fee set in 37 C.F.R. § 1.17 for the extension of time is \$420.00.

Please charge any additional fee required for this extension of time to Deposit Account 08-1480. A duplicate copy of this paper is enclosed.

Also filed is a Response to an Office Action.

Respectfully submitted,

10/08/2003 TBESHAH1 00000008 09914429

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420.00 OP

REED SMITH LLP 3110 Fairview Park Drive Suite 1400 Falls Church, Virginia 22042 (703) 641-4200 October 2, 2003 Stanley P. Fisher

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